Headphones

Electroacoustic Design and Verification

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	Earp	hone Tyj	oes	
Circumaural	Supra-aural	Supra-concha	Intra-concha	Insert
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 <u>Working Sound Pressure Level:</u> SPL resulting from a sinusoidal voltage (single figure at 500 Hz or simulated program signal) through the rated source impedance (120 ohms), across the input connector of the headphone, <i>at a level such that would cause 1 mW to be dissipated in a</i> <i>pure resistance equal to the rated impedance of the headphone.</i> 				
$\frac{\text{Voltages for 1 r}}{16 \Omega : 126 \text{ mV}}$ $20 \Omega : 141 \text{ mV}$ $25 \Omega : 158 \text{ mV}$ $32 \Omega : 179 \text{ mV}$ $40 \Omega : 200 \text{ mV}$ $50 \Omega : 224 \text{ mV}$	<u>nW for Nominal I</u> 60 Ω : 245 mV 80 Ω : 283 mV 100 Ω : 316 mV 120 Ω : 346 mV 160 Ω : 400 mV 200 Ω : 447 mV	Impedance, Z_0 : 250 Ω: 500 mV 300 Ω: 548 mV 400 Ω: 632 mV 500 Ω: 707 mV 600 Ω: 775 mV	Ohm's Law $V = IR, P = VI$ $P = \frac{V^2}{R} = \frac{V^2}{Z_0}$ $V^2 = PZ_0$	
 <u>Maximum Sound Pressure Level:</u> SPL produced in the ear simulator when the headphone is supplied with a sinusoidal voltage of the Rated Maximum Voltage at 500 Hz (see also EN 50332). © Copyright CJS Labs 2017 – San Francisco, CA USA www.cjs-labs 		$V = \sqrt{PZ_0} = \sqrt{0.001 \cdot Z_0}$ $V = 0.0316\sqrt{Z_0}$		

























































Conclusion (1) Insertion Gain A FLAT INSERTION GAIN is the target! This is 'corrected' from the measured response at DRP. Acoustic Impedance Low Acoustic Z = Open High Acoustic Z = Sealed Test System and Tests Requires a manikin equipped with calibrated ear simulator(s) Sine AND Noise stimuli may be required FFT Data requires 1/3 octave synthesis (power averaging) Most post-processing is simple dB subtraction Present data using the IEC 60263 preferred aspect ratio: 10, 25, or 50 dB = 1 decade

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